
Altera Quartus II 11.0 Crack [Extra Quality]



Quartus II software v10.0 also supports a new Build Enhancement Environment (BEE) workflow, which facilitates the debugging and development of projects across many Altera FPGAs and FPGA-on-ASIC architectures.

Quartus II software v10.0 includes BEE enhancements for Altera's Stratix V FPGA, C64x-72x, Virtex FPGAs, FPGA-on-ASIC, and SoC customer products. This includes enhancements to the software's auto-generated HDL code, Place & Route, and BEE project debugging tools. Quartus II software v10.0 also supports the latest Altera Quartus II software releases, including the new Quartus II IAR, Quartus II IAR Creator, Quartus II Simulator and Quartus II Simulator Creator. Quartus II software v10.0 supports:

SystemTap; Windows Debugging Tools for Windows (WDT) for Windows Vista, 2008 and Windows 7; and Device Discovery for Windows Vista, 2008 and Windows 7. Quartus II also adds support for the Altera EPM240 Evaluation Kit for Windows 2008 and Windows 7. For more information about these enhancements and all of the available product software, please see the Quartus II 10.0 Reference Guide. Altera's new Quartus II Simulator Source Explorer also provides a new project configuration capability. With a single click, the original Quartus II Software can be reconfigured using a project's CMC Content Mapping Control (CMC) and project file data. Quartus II software v10.0 includes a new standalone version of the Quartus II Simulator. Quartus II Simulator 12.0 enables users to develop and debug Quartus II programs on Altera FPGAs and ASICs, for development of systems-on-chip (SoC), general development and system-level verification. Quad SDK support for Quartus II Simulator 12.0 enables users to use Quartus II software without FPGA hardware for simulation and debug of SoC

designs.

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